

**APPLICATION DATA SHEET**

**Application Information**

Application number::	N/A
Filing Date::	October 24, 2001
Application Type::	Regular
Subject Matter::	Utility
Suggested classification::	N/A
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	N/A
Number of CD disks::	N/A
Number of copies of CDs::	N/A
Sequence submission?::	N/A
Computer Readable Form (CRF)?::	N/A
Number of copies of CRF::	N/A
Title ::	METHOD OF MANUFACTURING AN INTEGRATED SEMICONDUCTOR DEVICE HAVING A PLURALITY OF CONNECTION LEVELS
Attorney Docket Number::	854063.512D1
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	
Total Drawing Sheets::	4
Small Entity?::	No
Petition included?::	No
Petition Type::	N/A
Licensed U.S. Gov't Agency::	N/A
Contract or Grant No::	N/A
Secrecy Order in Parent Appl.?::	No

### **First Applicant Information**

Applicant Authority Type:: Inventor  
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Status:: Full Capacity  
Given Name:: Federico  
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State or Province of mailing address::  
Country of mailing address:: Italy  
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### **Correspondence Information**

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**Representative Information**

Representative Customer Number::		<b>00500</b>
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**Domestic Priority Information**

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	Divisional	09/405,506	09/23/99

**Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Europe	98830562.9	September 25, 1998	Yes

**Assignee Information**

Assignee name::	STMicroelectronics S.r.l.
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